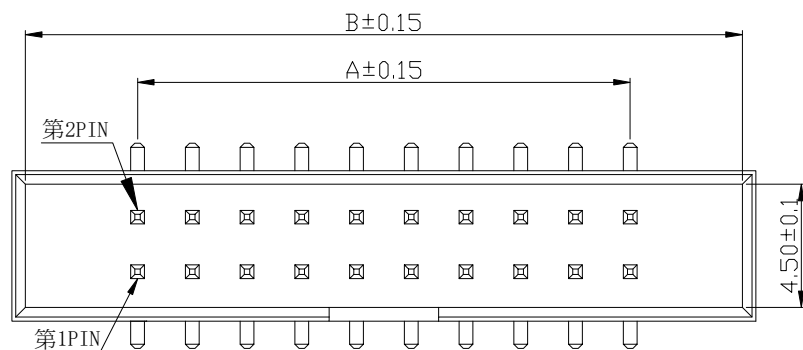
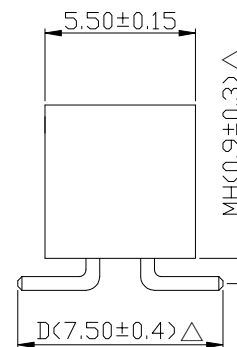
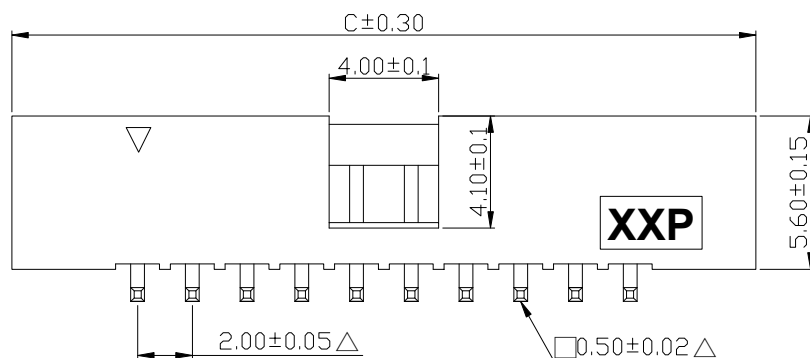


P.C.B Hole Layout

### Specifications

|                         |                              |
|-------------------------|------------------------------|
| Current Rating          | : 2 Amp                      |
| Insulator Resistance    | : 3000 Megohms Min.          |
| Dielectric Withstanding | : AC 500V                    |
| Operating Temperature   | : -40°C ~ +105°C             |
| Soldering Temperature   | : 260°C for 3~5s Max.        |
| Contact Material        | : Copper Alloy               |
| Insulator Material      | : NY-9T, UL94V-0             |
| Finish                  | : Tin, Gold or Duplex Plated |
| Standard                | : Gold Flash all over        |



| PIN NUMBER | A(mm) | B(mm) | C(mm) |
|------------|-------|-------|-------|
| 10PIN      | 8.00  | 16.20 | 17.20 |
| 12PIN      | 10.00 | 18.20 | 19.20 |
| 14PIN      | 12.00 | 20.20 | 21.20 |
| 16PIN      | 14.00 | 22.20 | 23.20 |
| 18PIN      | 16.00 | 24.20 | 25.20 |
| 20PIN      | 18.00 | 26.20 | 27.20 |
| 24PIN      | 22.00 | 30.20 | 31.20 |
| 26PIN      | 24.00 | 32.20 | 33.20 |
| 30PIN      | 28.00 | 36.20 | 37.20 |
| 34PIN      | 32.00 | 40.20 | 41.20 |
| 40PIN      | 38.00 | 46.20 | 47.20 |
| 44PIN      | 42.00 | 50.20 | 51.20 |
| 50PIN      | 48.00 | 56.20 | 57.20 |
| 68PIN      | 66.00 | 74.20 | 75.20 |



CHERNG WEEI

DESCRIPTION(内容):

2.0mm Box Header H:5.6 W:5.5 SMT □=0.50  
(RoHS)

PART NO:  
(料 號) CHEB2000XXS

DWG NO(圖號):

DRAWN(製圖):

黃炳秋

CHECKED(確認):

黃炳秋

APPROVAL(核准):

黃炳秋

GENERAL TOLERANCE:  
(公差)

.X .XXXX  
.XX .X°  
.XXX .XX°

SCALE(比例):

1:XX



REV:  
A/1

UNIT:  
mm

SIZE:  
A4

SHEET:  
1/1

REV

1

2

3

4

DESCRIPTION

△ 此符號表示重點管控尺寸

DATE

2025. 08. 26

A

B

C

D

E

FILE NO: CWJ-4E-